<u>REMARKS</u>

Claims 1-28 are pending in the instant application. Claims 1-28 are amended

herein. No new matter has been added as a result of the amendments made herein.

<u>Claim Objections</u>

Claims 2-10, 12-20, and 22-28 are objected to as containing informalities. Claims 2-10,

12-20, and 22-28 have been amended herein to address the objections raised by the Examiner.

Consequently, the objection to Claims 2-10, 12-20 and 22-28 should be withdrawn.

<u>112 Rejections</u>

Claims 1, 11, and 21 are rejected under 35 U.S.C 112, second paragraph, as being

indefinite. Claims 1, 11, and 21 have been amended here to address the 112 rejections raised by

the Examiner. Consequently, the rejection of Claims 1, 11 and 21 should be withdrawn.

<u>103 Rejections</u>

Claims 1, 5-7, 10, 11, 15-17, 20, 21, and 25-27 are rejected under 35 U.S.C. § 103(a) as

being unpatentable over Wisor (US Patent No. 5,422,862) in view of Goff et al. (US Patent No.

6, 105,142).

<u>Independent Claim 1</u>

The Examiner is respectfully directed to independent Claim 1 which is drawn to an

embodiment of the present invention that includes an alarm clock IC adapted for use in a

personal computer (PC) having a system Real Time Clock (RTC), that comprises:

> ...alarm clock logic circuitry adapted to receive a
> signal indicative of the a power status of said PC,

<u>adapted to adjust time inside said alarm clock IC to</u>
<u>match with said system RTC</u>, and further adapted to
generate an alarm clock event at a preselected time.

Applicants respectfully submit that neither the Wisor reference nor the Goff reference

anticipates or renders obvious an alarm clock IC adapted to adjust time inside the alarm clock IC

to match with the system RTC and further adapted to generate an alarm clock event as presently

claimed in amended Claim 1.

Moreover, Applicants respectfully submit that, in the Wisor reference, although a Real

Time Clock (RTC) is provided for tracking the current time and is also associated with an alarm

mechanism, the alarm clock logic circuitry of the present invention should not be interpreted as

the RTC of the computer system. The alarm clock logic circuitry of the present invention is

separated from the system RTC circuit and is able to adjust the time inside the circuitry to match

with the system RTC. After reviewing the Wisor reference, Applicants respectfully submit that

Wisor only disclose a RTC circuit 104, which is a system clock circuit, and thus the alarm clock

logic circuitry in the claimed invention is not disclosed in the Wisor reference.

Goff does not teach or suggest a modification of Wisor that would remedy the

deficiencies of Wisor outlined above. More specifically, nowhere in the Goff reference is an

alarm clock logic circuit similar to that recited in Claim 1 taught or suggested. The clock

disclosed in the Goff is a dissimilar system Real Time Clock (RTC) that is very different from

the alarm clock logic circuit of the claimed invention. Consequently, the embodiments of the

Applicant's invention as are set forth in Claim 1 are neither anticipated nor rendered obvious by

Goff.

Thus, Applicants respectfully submit that the present invention as disclosed in independent Claim 1 is not anticipated by the Wisor reference taken alone or in combination with the Goff reference, and is in a condition for allowance.

As a result, Applicants respectfully submit that Claims 5-7 and 10 which depend from independent Claim 1 are also in a condition for allowance as being dependent on an allowable base claim.

Independent Claim 11

The Examiner is respectfully directed to independent Claim 11 which is drawn to an embodiment of the present invention that includes an alarm clock PC system, that comprises:

> ...an alarm clock IC adapted to receive said signal indicative of the power status of said PC, adapted to adjust time inside said alarm clock IC to match with said system RTC, and further adapted to generate an alarm clock event at a preselected time...(Emphasis Added)

Applicants respectfully submit that neither the Wisor reference nor the Goff reference anticipates or renders obvious an alarm clock IC adapted to adjust time inside the alarm clock IC to match with the system RTC and further adapted to generate an alarm clock event as presently claimed in amended Claim 11.

It should be appreciated that independent Claim 11 contains limitations similar to those set forth in Claim 1 and, that these limitations are distinctly different from and non-obvious over the subject matter disclosed by the Wisor reference, the Goff reference, and their combination.

Additionally, Applicants respectfully submit that Claims 15-17 and 20 which depend from independent Claim 11 are also in a condition for allowance as being dependent on an allowable base claim.

Independent Claim 21

The Examiner is respectfully directed to independent Claim 21 which is drawn to an embodiment of the present invention that includes a method of operating a PC as an alarm clock, that comprises:

> ...monitoring a power status of said PC; <u>matching time of said alarm clock with a system Real Time Clock (RTC)</u>; and generating an alarm clock event at a preselected time.

Applicants respectfully submit that neither the Wisor reference nor the Goff reference anticipates or renders obvious the steps of matching time of the alarm clock with a system RTC, and generating an alarm clock event at a preselected time as presently claimed in amended Claim 21.

It should be appreciated that dependent Claim 21 contains limitations similar to those set forth in Claim 1 and, that these limitations are distinctly different from and non-obvious over the subject matter disclosed by the Wisor reference, the Goff reference, and their combination.

Additionally, Applicants respectfully submit that Claims 25-27 which depend from independent Claim 21 are also in a condition for allowance as being dependent on an allowable base claim.

Claims 2, 4, 12, 14, 22 and 24 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Wisor (US Patent No. 5,422,862) in view of Goff et al. (US Patent No. 6, 105,142) in further view of Bolleman et al. (US Patent No. 6,268, 063).

Bolleman et al. does not teach or suggest a modification of Wisor and Goff et al. that would remedy the deficiencies of Wisor and Goff et al. outlined above. More specifically, Bolleman et al. does not teach an alarm clock IC comprising alarm clock logic circuitry adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 1 (from which Claims 2 and 4 depend). Consequently, Wisor in view of Goff et al. in further view of Bolleman et al. does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claims 2 and 4.

Similarly, Bolleman et al. does not teach an alarm clock PC system comprising an alarm clock IC adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 11 (from which Claims 12 and 14 depend). Consequently, Wisor in view of Goff et al. in further view of Bolleman et al. does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claims 12 and 14.

Similarly, Bolleman et al. does not teach a method of operating a PC as an alarm clock comprising matching time of said alarm clock with a system Real time Clock (RTC) as is recited in Claim 21 (from which Claim 24 depends). Consequently, Wisor in view of Goff et al. in further view of Bolleman et al. does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 24.

Claims 3, 13 and 23 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Wisor (US Patent No. 5,422,862) in view of Goff et al. (US Patent No. 6, 105,142) in further view of Brusky (US Patent No. 6,284, 406).

Brusky does not teach or suggest a modification of Wisor and Goff et al. that would remedy the deficiencies of Wisor and Goff et al. outlined above. More specifically, Brusky does not teach an alarm clock IC comprising alarm clock logic circuitry adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 1 (from which Claim 3 depends). Consequently, Wisor in view of Goff et al. in further view of Brusky does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 3.

Similarly, Brusky does not teach an alarm clock PC system comprising an alarm clock IC adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 11 (from which Claim 13 depends). Consequently, Wisor in view of Goff et al. in further view of Brusky does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 13.

Similarly, Brusky does not teach a method of operating a PC as an alarm clock comprising matching time of said alarm clock with a system Real time Clock (RTC) as is recited in Claim 21 (from which Claim 23 depends). Consequently, Wisor in view of Goff et al. in further view of Brusky does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 23.

Claims 8 and 18 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Wisor (US Patent No. 5,422,862) in view of Goff et al. (US Patent No. 6, 105,142) in further view of Tomiyasu (US Patent No. 6,134,187).

Tomiyasu does not teach or suggest a modification of Wisor and Goff et al. that would remedy the deficiencies of Wisor and Goff et al. outlined above. More specifically, Tomiyasu does not teach an alarm clock IC comprising alarm clock logic circuitry adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 1 (from which Claim 8 depends). Consequently, Wisor in view of Goff et al. in further view of Tomiyasu does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 8.

Similarly, Tomiyasu does not teach an alarm clock PC system comprising an alarm clock IC adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 11 (from which Claim 18 depends). Consequently, Wisor in view of Goff et al. in further view of Tomiyasu does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 18.

Claims 9, 19 and 28 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Wisor (US Patent No. 5,422,862) in view of Goff et al. (US Patent No. 6, 105,142) in further view of Seo (US Patent No. 5, 758, 172).

Seo does not teach or suggest a modification of Wisor and Goff et al. that would remedy the deficiencies of Wisor and Goff et al. outlined above. More specifically, Seo does not teach an alarm clock IC comprising alarm clock logic circuitry adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 1 (from which Claim 9 depends). Consequently, Wisor in view of Goff et al. in further view of Seo does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 9.

Similarly, Seo does not teach an alarm clock PC system comprising an alarm clock IC adapted to adjust time inside said alarm clock IC to match with said system RTC as is recited in Claim 11 (from which Claim 19 depends). Consequently, Wisor in view of Goff et al. in further view of Seo does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 19.

Similarly, Seo does not teach a method of operating a PC as an alarm clock comprising matching time of said alarm clock with a system Real time Clock (RTC) as is recited in Claim 21 (from which Claim 28 depends). Consequently, Wisor in view of Goff et al. in further view of Seo does not anticipate or render obvious the embodiments of Applicants' invention as set forth in Claim 28.

Conclusion

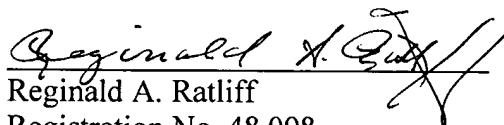In light of the above-listed remarks, the Applicants respectfully request allowance of the remaining Claims.

Based on the arguments presented above, Applicants respectfully assert that Claims 1-28 overcome the rejections of record. Therefore, Applicants respectfully solicit allowance of these Claims.

The Examiner is urged to contact the Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application.

Respectfully submitted,

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